

In Place of FORM PTO-1449 (Modified)

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Applicant:

Christopher L. Hamlin

Filing Date:

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Group:

282

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**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT**

Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
AAA						
ABA						
ACA						
ADA						
AEA						
AFA						
AGA						
AHA						
AIA						
AJA						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
AKA						
ALA						
AMA						
ANA						
AOA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner

Initial

NZ APA Reliability in CMOS IC Design: Physical Failure Mechanisms and their Modeling
NL AQA MOSIS Scalable CMOS (SCMOS) Design Rules (Revision 8.0) updated April 25, 2003;
NL ARA <http://www.mosis.org/Technical/Designrules/scmos/scmos-main.html>; 04/30/03

Examiner:

Nam 19

Date Considered:

04/22/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.